



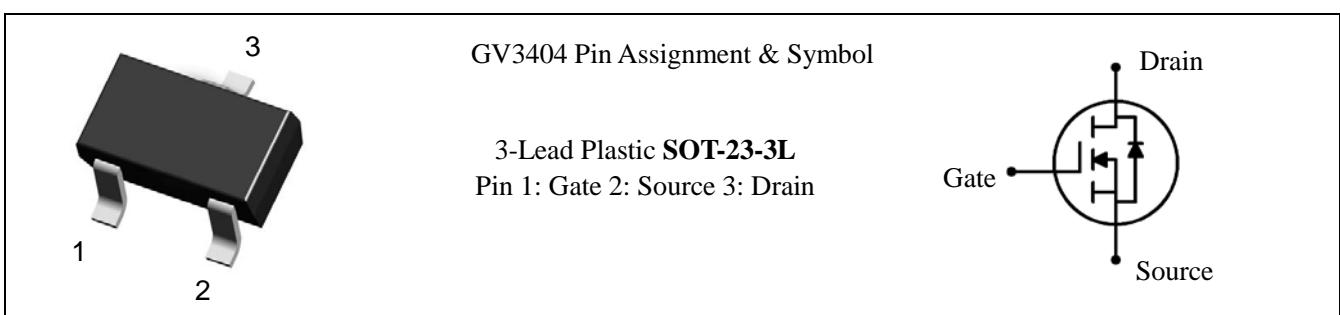
### N-Channel Enhancement-Mode MOSFET (30V, 5.8A)

PRODUCT SUMMARY		
V <sub>DSS</sub>	I <sub>D</sub>	R <sub>DS(on)</sub> (m-ohm) Max
30V	5.8A	28 @ V <sub>GS</sub> = 10V, I <sub>D</sub> =5.8A
		43 @ V <sub>GS</sub> = 4.5V, I <sub>D</sub> =5.0A

### Features

- Super high dense cell trench design for low R<sub>DS(on)</sub>.
- Rugged and reliable.
- SOT-23-3L package
- Ordering information: GV3404-G(Lead(Pb)-free and halogen-free)

RoHS+HF



### Absolute Maximum Ratings (T<sub>A</sub>=25°C, unless otherwise noted)

Symbol	Parameter	Ratings	Units
V <sub>DS</sub>	Drain-Source Voltage	30	V
V <sub>GS</sub>	Gate-Source Voltage	±20	V
I <sub>D</sub>	Drain Current (Continuous) @T <sub>A</sub> =25°C	5.8	A
I <sub>DM</sub>	Drain Current (Pulsed) <sup>a</sup>	20	A
P <sub>D</sub>	Total Power Dissipation @T <sub>A</sub> =25°C	1.4	W
T <sub>j</sub> , T <sub>stg</sub>	Operating Junction and Storage Temperature Range	-55 to +150	°C
R <sub>θJA</sub>	Thermal Resistance Junction to Ambient (PCB mounted) <sup>b</sup>	90	°C/W

Note: a: Repetitive Rating: Pulse width limited by the maximum junction temperature.  
b: 1-in<sup>2</sup> 2oz Cu PCB board

**Electrical Characteristics** ( $T_A=25^\circ\text{C}$ , unless otherwise noted)

Symbol	Characteristic	Test Conditions	Min.	Typ.	Max.	Unit
<b>• Off Characteristics</b>						
$\text{BV}_{\text{DSS}}$	Drain-Source Breakdown Voltage	$V_{\text{GS}}=0\text{V}, I_{\text{D}}=250\mu\text{A}$	30	-	-	V
$I_{\text{DSS}}$	Zero Gate Voltage Drain Current	$V_{\text{DS}}=30\text{V}, V_{\text{GS}}=0\text{V}$	-	-	1	$\mu\text{A}$
		$V_{\text{DS}}=24\text{V}, V_{\text{GS}}=0\text{V}, T_J=55^\circ\text{C}$	-	-	5	
$I_{\text{GSS}}$	Gate-Body Leakage Current	$V_{\text{GS}}=\pm 20\text{V}, V_{\text{DS}}=0\text{V}$	-	-	$\pm 100$	nA
<b>• On Characteristics</b> <sup>c</sup>						
$V_{\text{GS(th)}}$	Gate Threshold Voltage	$V_{\text{DS}}=V_{\text{GS}}, I_{\text{D}}=250\mu\text{A}$	1	-	3	V
$I_{\text{D(on)}}$	On state drain current	$V_{\text{GS}}=4.5\text{V}, V_{\text{DS}}=5\text{V}$	20	-	-	A
$R_{\text{DS(on)}}$	Drain-Source On-State Resistance	$V_{\text{GS}}=10\text{V}, I_{\text{D}}=5.8\text{A}$	-	22.5	28	$\text{m}\Omega$
		$V_{\text{GS}}=10\text{V}, I_{\text{D}}=5.8\text{A}, T_J=125^\circ\text{C}$	-	31.3	38	
		$V_{\text{GS}}=4.5\text{V}, I_{\text{D}}=5\text{A}$	-	34.5	43	
$g_{\text{FS}}$	Forward Transconductance	$V_{\text{DS}}=5\text{V}, I_{\text{D}}=5.8\text{A}$	-	14.5	-	S
<b>• Dynamic Characteristics</b> <sup>d</sup>						
$C_{\text{iss}}$	Input Capacitance	$V_{\text{DS}}=15\text{V}, V_{\text{GS}}=0\text{V}, f=1\text{MHz}$	-	680	-	$\text{pF}$
$C_{\text{oss}}$	Output Capacitance		-	102	-	
$C_{\text{rss}}$	Reverse Transfer Capacitance		-	77	-	
$R_g$	Gate resistance	$V_{\text{GS}}=0\text{V}, V_{\text{DS}}=0\text{V}, f=1\text{MHz}$	-	3	-	$\Omega$
<b>• Switching Characteristics</b> <sup>d</sup>						
$Q_g(10)$	Total Gate Charge(10V)	$V_{\text{DS}}=15\text{V}, I_{\text{D}}=5.8\text{A}, V_{\text{GS}}=10\text{V}$	-	13.88	-	$\text{nC}$
$Q_g(4.5)$	Total Gate Charge(4.5V)		-	6.78	-	
$Q_{\text{gs}}$	Gate-Source Charge		-	1.8	-	
$Q_{\text{gd}}$	Gate-Drain Charge		-	3.12	-	
$t_{\text{d(on)}}$	Turn-on Delay Time	$V_{\text{DS}}=15\text{V}, R_L=2.7\Omega, V_{\text{GS}}=10\text{V}, R_{\text{GEN}}=3\Omega$	-	4.6	-	$\text{nS}$
$t_r$	Turn-on Rise Time		-	3.8	-	
$t_{\text{d(off)}}$	Turn-off Delay Time		-	20.9	-	
$t_f$	Turn-off Fall Time		-	5	-	
$t_{\text{rr}}$	Body Diode Reverse Recovery Time	$I_F=5.8\text{A}, dI/dt=100\text{A}/\mu\text{s}$	-	16.1	-	nS
$Q_{\text{rr}}$	Body Diode Reverse Recovery Charge	$I_F=5.8\text{A}, dI/dt=100\text{A}/\mu\text{s}$	-	7.4	-	nC
<b>• Drain-Source Diode Characteristics</b>						
$V_{\text{SD}}$	Drain-Source Diode Forward Voltage	$V_{\text{GS}}=0\text{V}, I_S=1\text{A}$	-	-	1	V
$I_S$	Maximum Body-Diode Continuous Current	-	-	2.5	A	
	Pulsed Body-Diode Current	-	-	20	A	

Note: c: Pulse Test : Pulse Width &lt; 300μs, Duty Cycle &lt; 2%

d: Guaranteed by design, not subject to production testing.

### Characteristics Curve

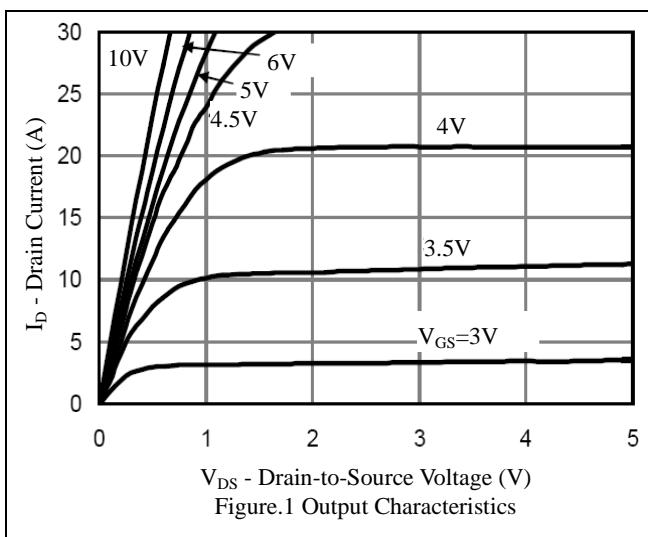


Figure.1 Output Characteristics

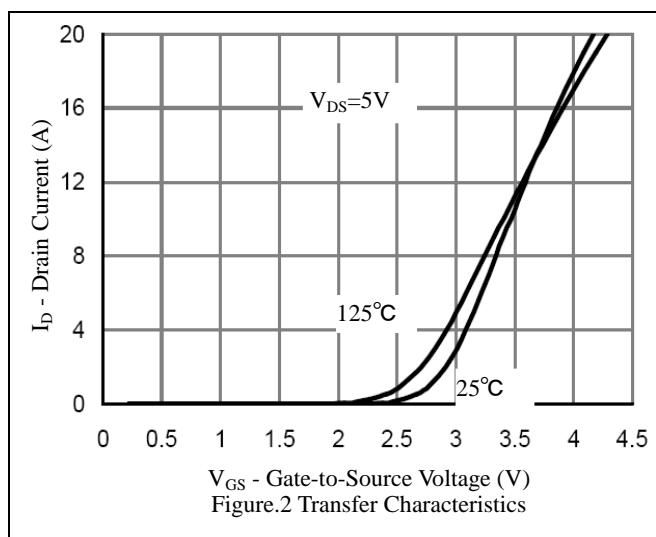


Figure.2 Transfer Characteristics

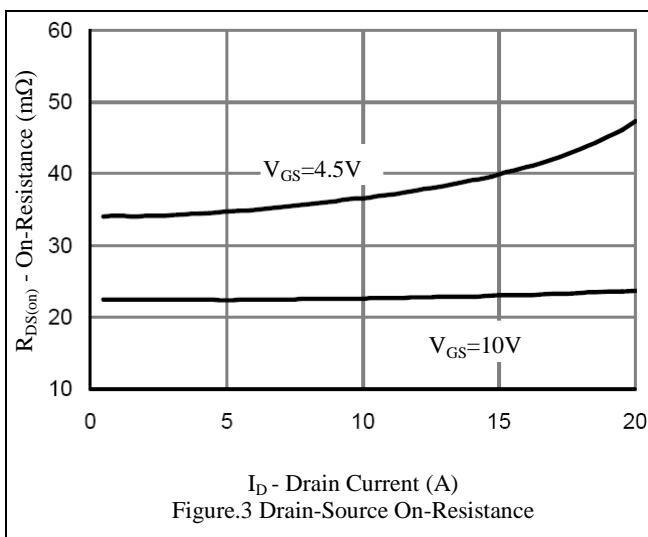


Figure.3 Drain-Source On-Resistance

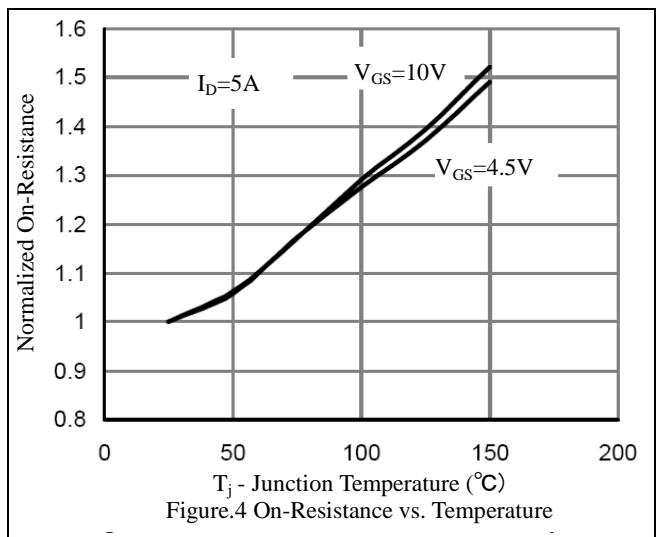


Figure.4 On-Resistance vs. Temperature

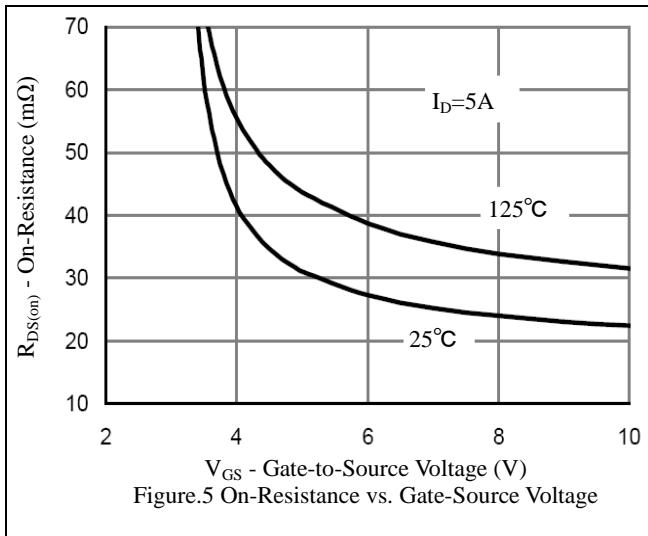


Figure.5 On-Resistance vs. Gate-Source Voltage

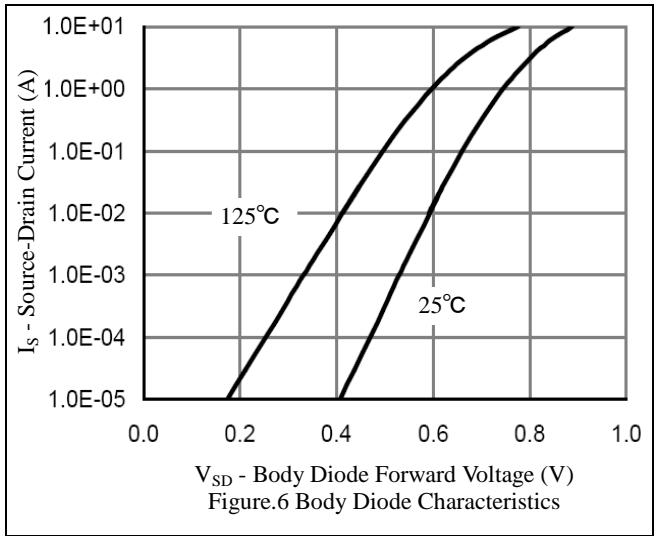


Figure.6 Body Diode Characteristics

### Characteristics Curve

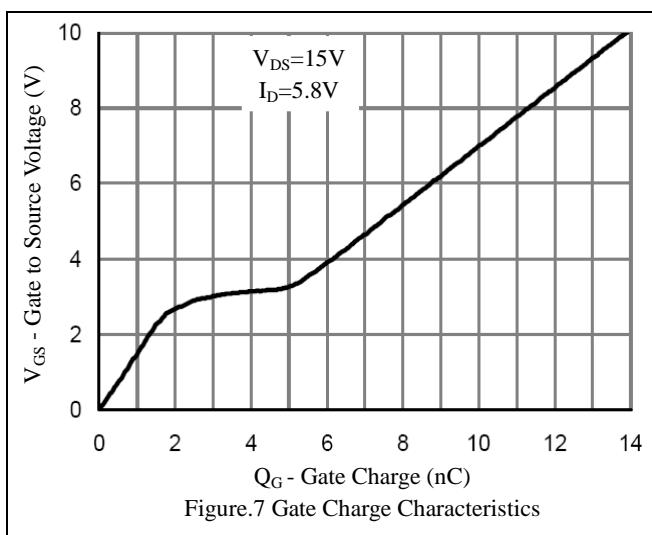


Figure.7 Gate Charge Characteristics

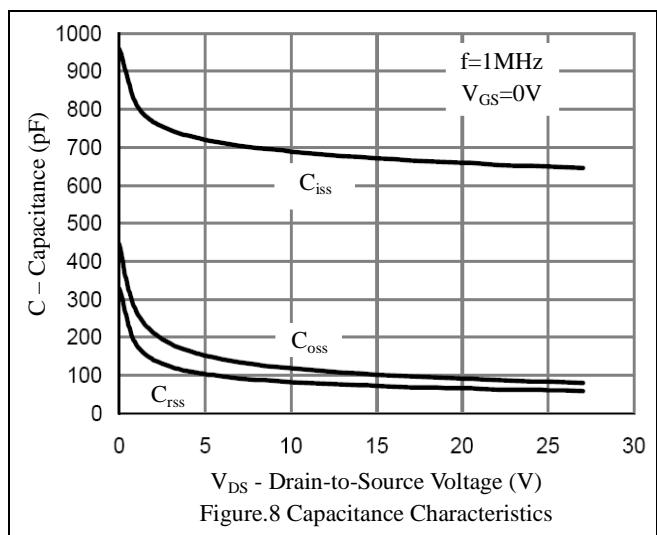


Figure.8 Capacitance Characteristics

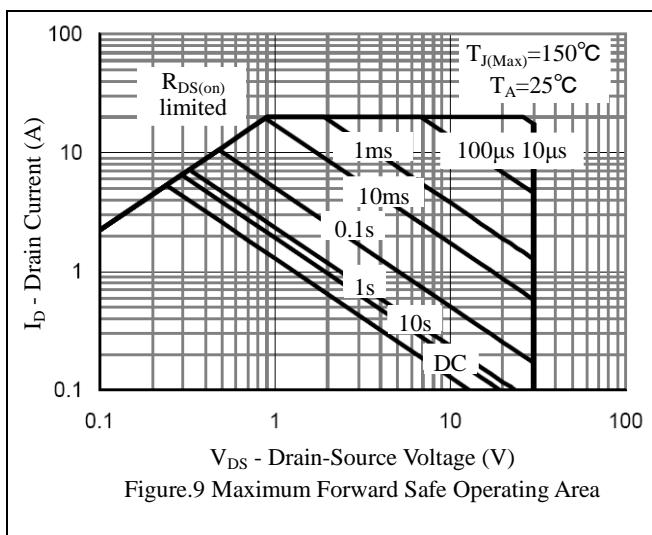


Figure.9 Maximum Forward Safe Operating Area

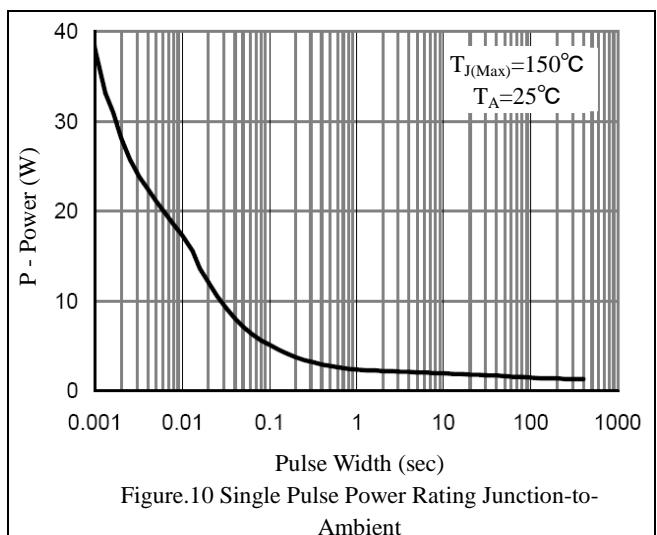


Figure.10 Single Pulse Power Rating Junction-to-Ambient

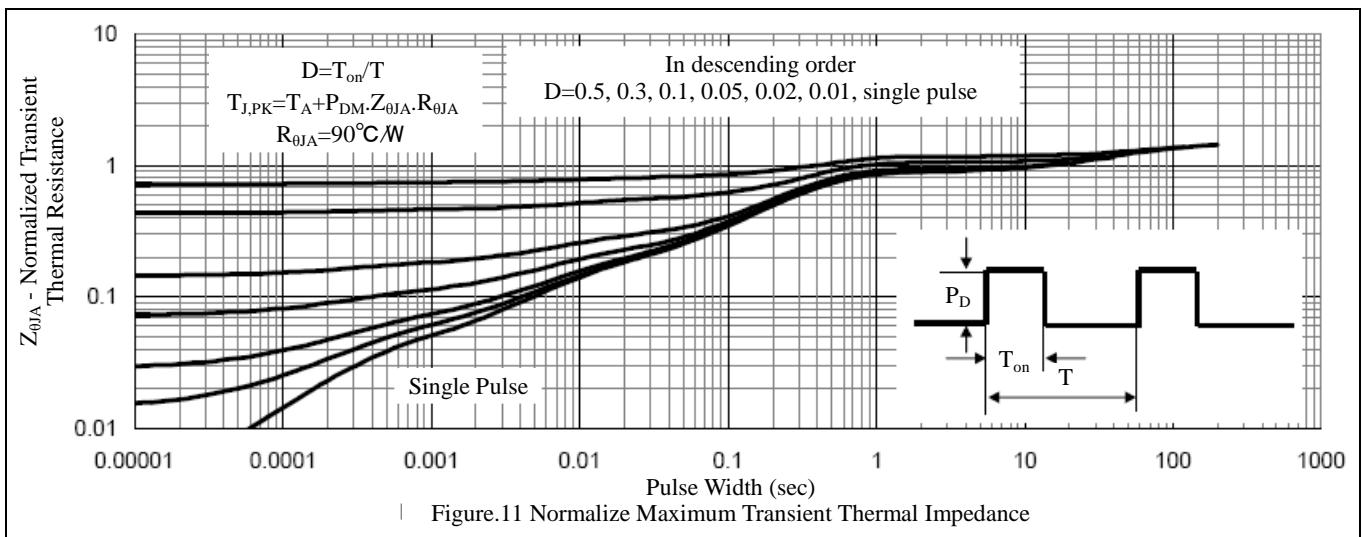
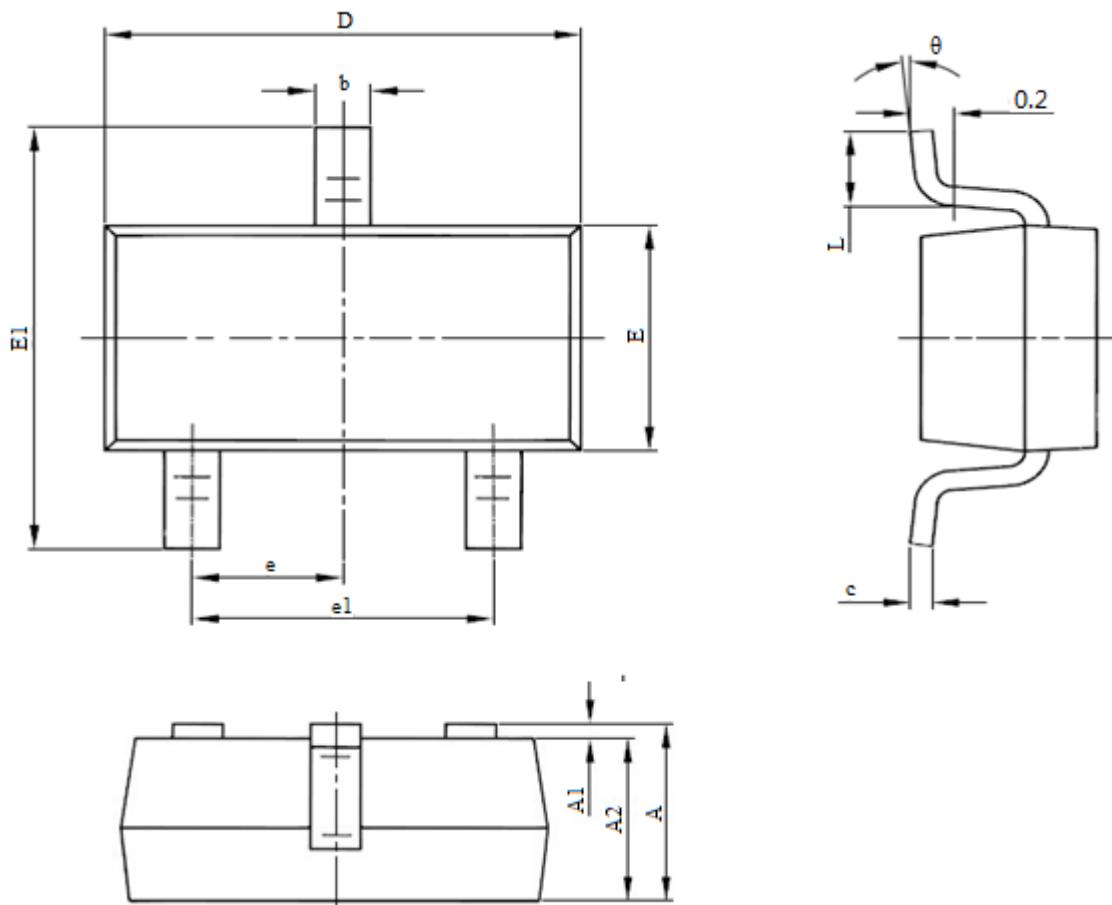


Figure.11 Normalize Maximum Transient Thermal Impedance

## SOT-23 PACKAGE OUTLINE DIMENSIONS



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min	Max	Min	Max
A	0.85	1.25	0.033	0.049
A1	0	0.1	0	0.004
A2	0.7	1.15	0.028	0.045
b	0.3	0.5	0.012	0.020
c	0.1	0.2	0.004	0.008
D	2.82	3.02	0.111	0.119
E	1.5	1.7	0.059	0.067
E1	2.65	2.95	0.104	0.116
e	0.95(BSC)		0.037(BSC)	
e1	1.8	2	0.071	0.079
L	0.3	0.6	0.012	0.024
θ	0°	8°	0°	8°

### Notice

1. Specification of the products displayed herein is subject to change without notice. Continuous development may necessitate changes in technical data without notice. GEMMICRO or anyone on its behalf assumes no responsibility or liability for any errors or inaccuracies.
2. Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications are not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.