

Complementary High Density Trench MOSFET

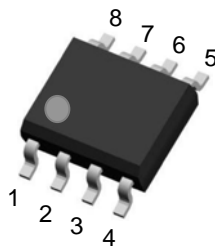
PRODUCT SUMMARY (N-Channel)		
V _{DSS}	I _D	R _{DS(on)} (mΩ) Max
30V	10A	13.5 @ V _{GS} = 10 V, I _D =10A
		20 @ V _{GS} = 4.5V, I _D =5A

PRODUCT SUMMARY (P-Channel)		
V _{DSS}	I _D	R _{DS(on)} (mΩ) Max
-30V	-9.1A	20 @ V _{GS} = -10V, I _D =-9.1A
		35 @ V _{GS} = -4.5V, I _D =-5A

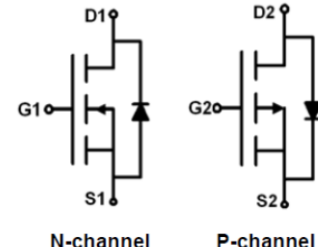
Features

- Advanced Trench Process Technology
- High Density Cell Design for Ultra Low On-Resistance
- Surface mount Package
- Ordering information: GS3225(Lead(Pb)-free and halogen-free)

RoHS+HF



Pin 1: Source 1
 Pin 2: Gate 1
 Pin 3: Source 2
 Pin 4: Gate 2
 Pin 5 / 6: Drain 2
 Pin 7 / 8: Drain 1



N-channel P-channel

SOP-8

Absolute Maximum Ratings (T_A=25°C, unless otherwise noted)

Symbol	Parameter	N-Channel	P-Channel	Units
V _{DS}	Drain-Source Voltage	30	-30	V
V _{GS}	Gate-Source Voltage	±20	±20	V
I _D	Drain Current ^a	10	-9.1	A
I _{DM}	Drain Current ^b (Pulsed)	40	-32	A
P _D	Total Power Dissipation ^a @T _A =25°C	2.5	2.5	W
	Total Power Dissipation ^a @T _A =75°C	1.2	1.2	
T _j , T _{stg}	Operating Junction and Storage Temperature Range ^a	-55 to +150	-55 to +150	°C
R _{θJA}	Thermal Resistance Junction to Ambient ^a	63.2	63.2	°C/W

Note: a: Surface Mounted on FR4 Board, t ≤ 5sec.

b: Pulse width limited by maximum junction temperature.

N-Channel Electrical Characteristics (T_A=25 °C, unless otherwise noted)

Symbol	Characteristic	Test Conditions	Min.	Typ.	Max.	Unit
• Off Characteristics						
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V, I _D =250μA	30	-	-	V
I _{DSS}	Zero Gate Voltage Drain Current	V _{DS} =24V, V _{GS} =0V	-	-	-1	μA
I _{GSS}	Gate-Body Leakage Current	V _{GS} =±20V, V _{DS} =0V	-	-	±100	nA
• On Characteristics^c						
V _{GS(th)}	Gate Threshold Voltage	V _{DS} =V _{GS} , I _D =250μA	1.0	-	3.0	V
R _{DS(on)}	Drain-Source On-State Resistance	V _{GS} =10V, I _D = 10A	-	7.5	13.5	mΩ
		V _{GS} =4.5V, I _D = 5.0A	-	11	20	
g _{fs}	Forward Transconductance	V _{DS} =5V, I _D = 5.0A	15	-	-	S
• Dynamic Characteristics^d						
C _{iss}	Input Capacitance	V _{DS} =15V, V _{GS} =0V, f=1MHz	-	1550	-	pF
C _{oss}	Output Capacitance		-	300	-	
C _{rss}	Reverse Transfer Capacitance		-	180	-	
• Switching Characteristics^d						
Q _g	Total Gate Charge	V _{DS} =10V, I _D =1A, V _{GS} =10V	-	11	-	nC
Q _{gs}	Gate-Source Charge		-	5.2	-	
Q _{gd}	Gate-Drain Charge		-	3.3	-	
t _{d(on)}	Turn-on Delay Time	V _{DD} = 15V, R _L =15Ω, I _D =1A, V _{GEN} =10V, R _G =6Ω	-	29	-	nS
t _r	Turn-on Rise Time		-	18	-	
t _{d(off)}	Turn-off Delay Time		-	92	-	
t _f	Turn-off Fall Time		-	76	-	
• Drain-Source Diode Characteristics						
V _{SD}	Drain-Source Diode Forward Voltage	V _{GS} =0V, I _S =2.5A	-	-	1.2	V

Note: c: Pulse width limited by maximum junction temperature.

d: Guaranteed by design, not subject to production testing.

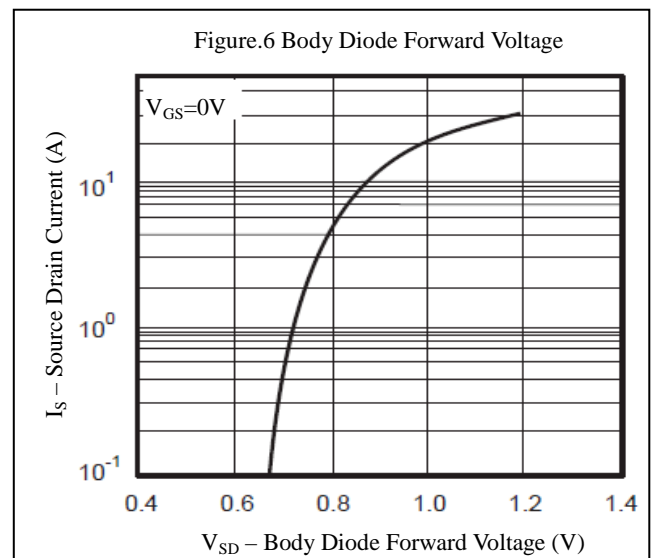
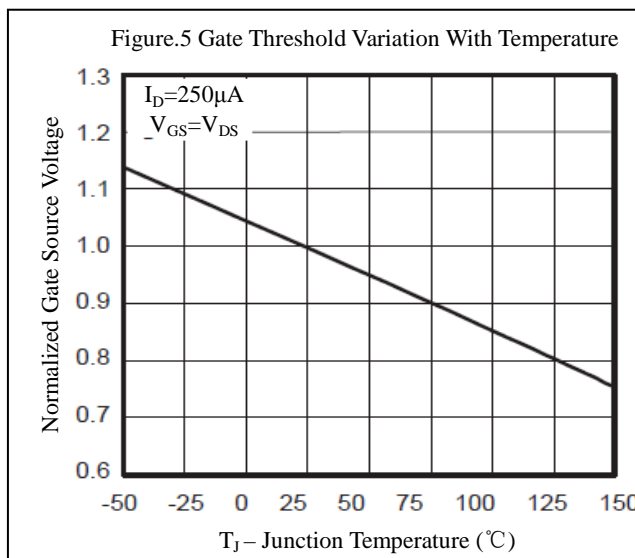
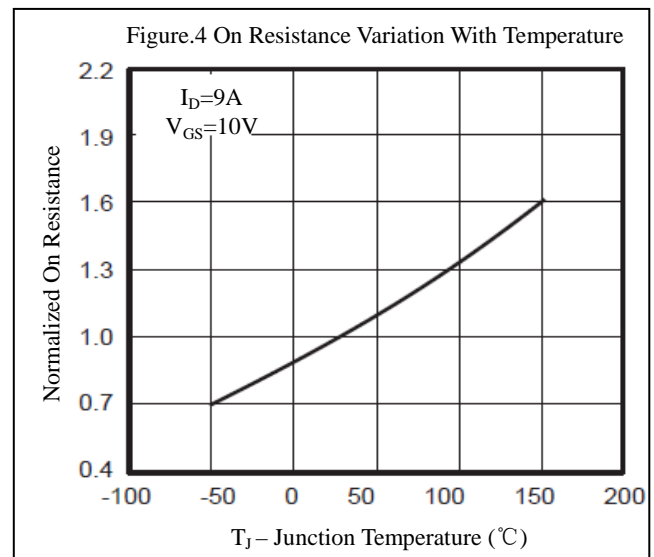
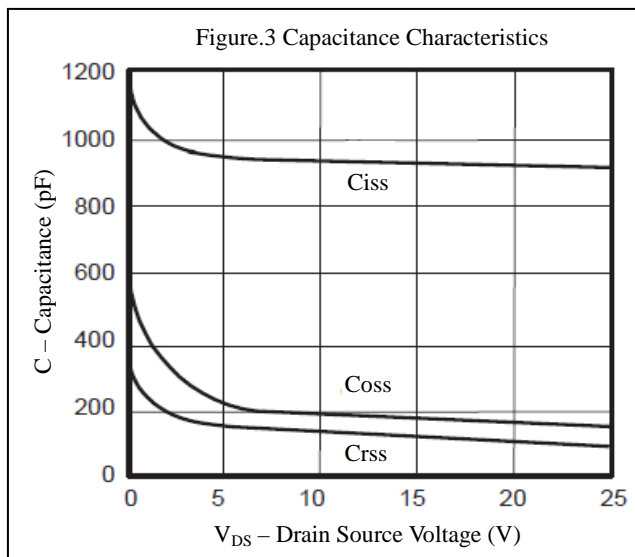
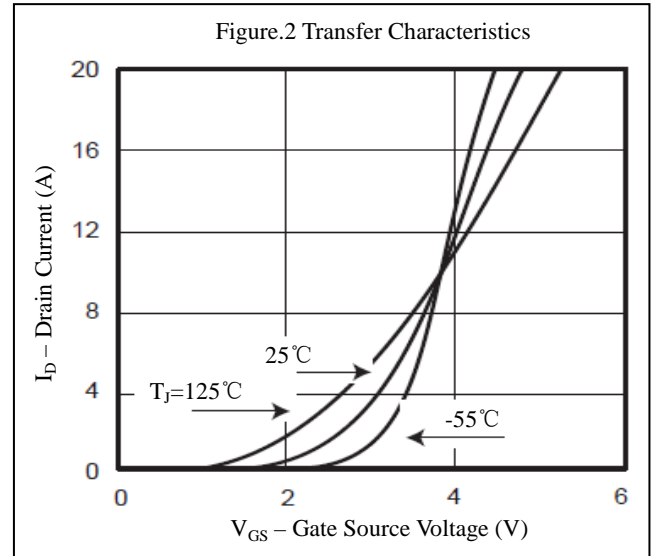
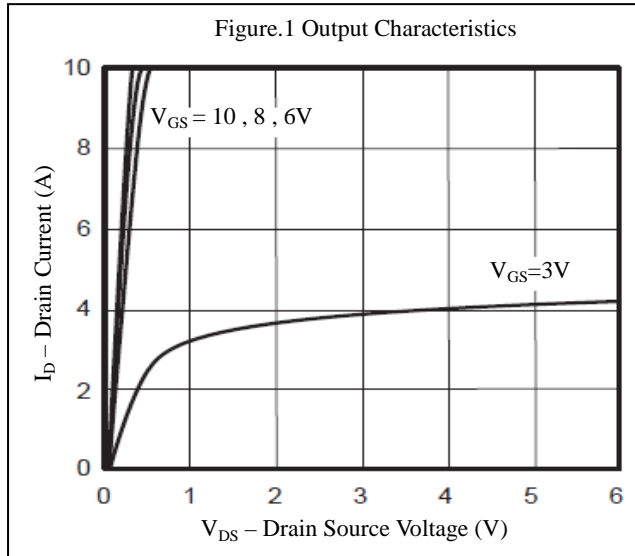
P-Channel Electrical Characteristics (T_A=25°C, unless otherwise noted)

Symbol	Characteristic	Test Conditions	Min.	Typ.	Max.	Unit
• Off Characteristics						
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V, I _D =-250μA	-30	-	-	V
I _{DSS}	Zero Gate Voltage Drain Current	V _{DS} =-24V, V _{GS} =0V	-	-	-1	μA
I _{GSS}	Gate-Body Leakage Current	V _{GS} =±20V, V _{DS} =0V	-	-	±100	nA
• On Characteristics^c						
V _{GS(th)}	Gate Threshold Voltage	V _{DS} =V _{GS} , I _D =-250μA	-1.0	-	-3.0	V
R _{DS(on)}	Drain-Source On-State Resistance	V _{GS} =-10V, I _D =-6.0A	-	15	20	mΩ
		V _{GS} =-4.5V, I _D =-5.0A	-	21	35	
g _{fs}	Forward Transconductance	V _{DS} =-10V, I _D =-6.0A	-	12.7	-	S
• Dynamic Characteristics^d						
C _{iss}	Input Capacitance	V _{DS} =-15V, V _{GS} =0V, f=1MHz	-	1570	-	pF
C _{oss}	Output Capacitance		-	355	-	
C _{rss}	Reverse Transfer Capacitance		-	310	-	
• Switching Characteristics^d						
Q _g	Total Gate Charge	V _{DS} =-15V, I _D =-3A, V _{GS} =-10V	-	27	-	nC
Q _{gs}	Gate-Source Charge		-	5.8	-	
Q _{gd}	Gate-Drain Charge		-	8	-	
t _{d(on)}	Turn-on Delay Time	V _{DD} =-15V, R _L =5Ω, I _D =-3A, V _{GEN} =-10V, R _G =6Ω	-	9.5	-	nS
t _r	Turn-on Rise Time		-	14	-	
t _{d(off)}	Turn-off Delay Time		-	99.5	-	
t _f	Turn-off Fall Time		-	63.6	-	
• Drain-Source Diode Characteristics						
V _{SD}	Drain-Source Diode Forward Voltage	V _{GS} =0V, I _S =-2.3A	-	-	-1.2	V

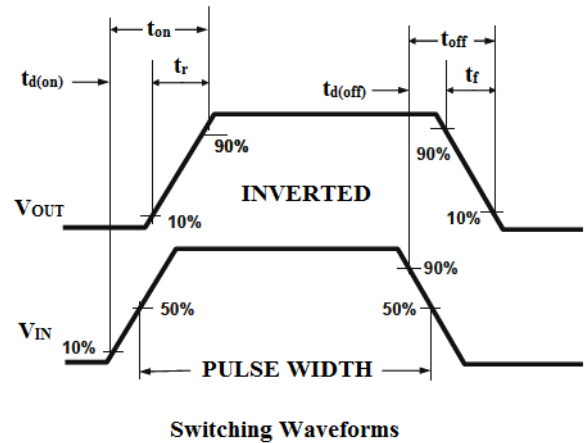
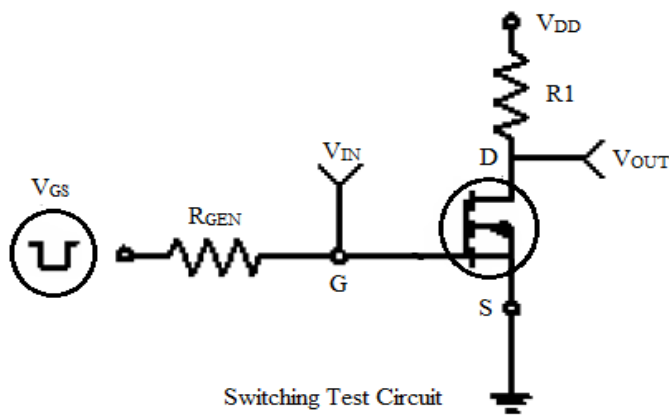
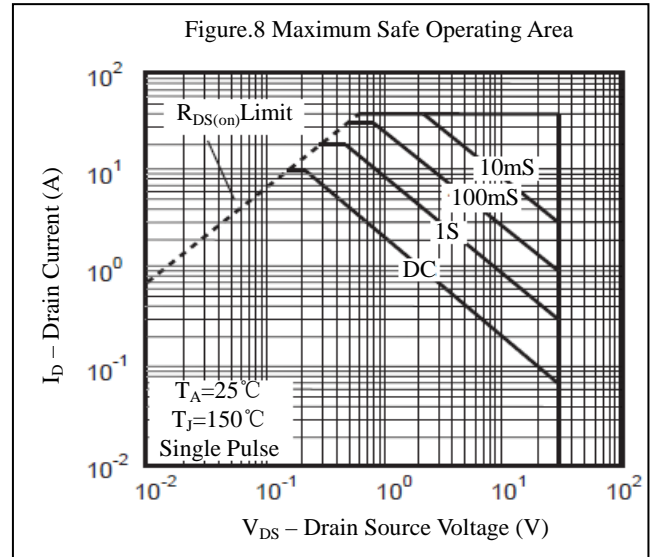
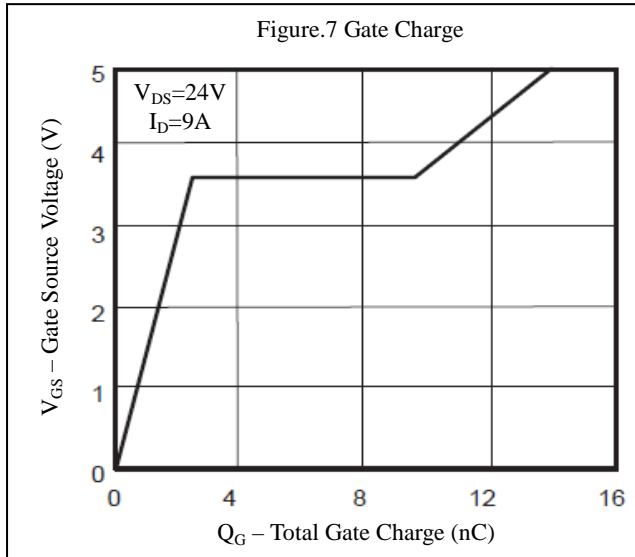
Note:b: Pulse width limited by maximum junction temperature.

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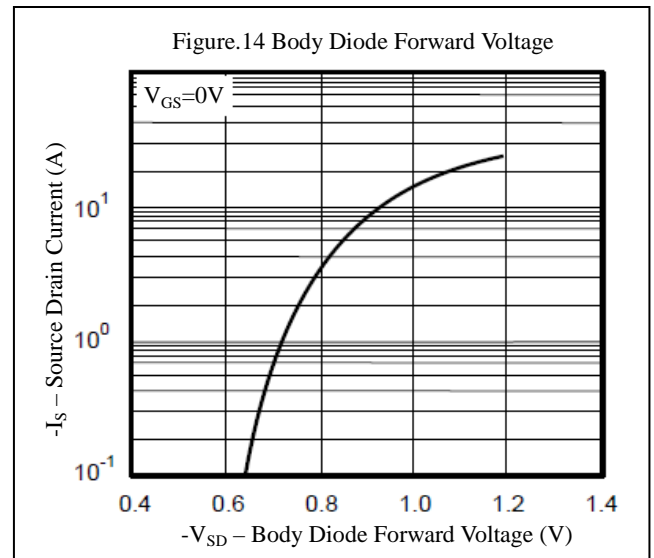
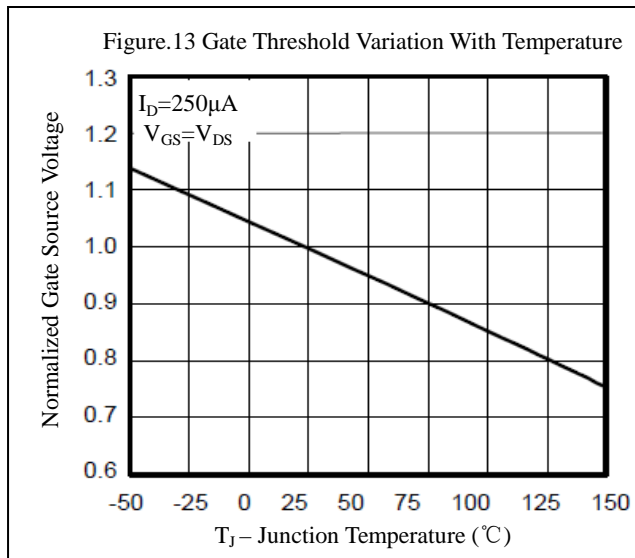
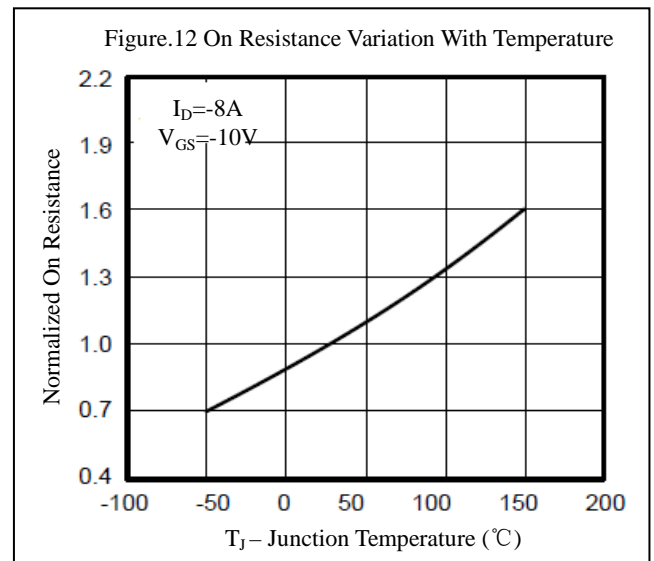
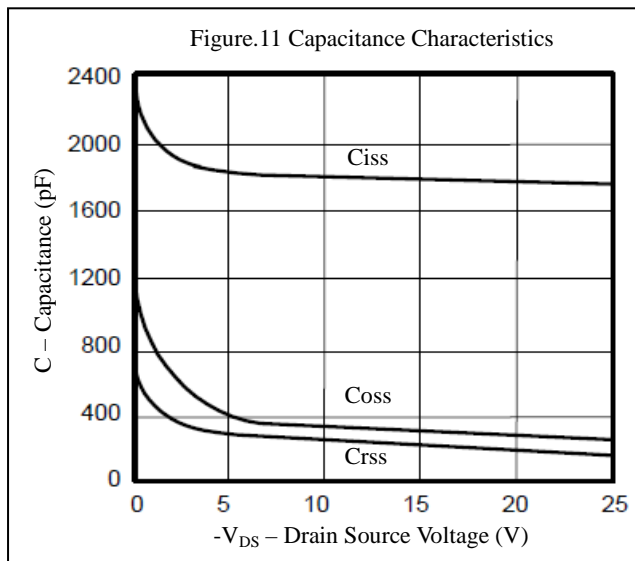
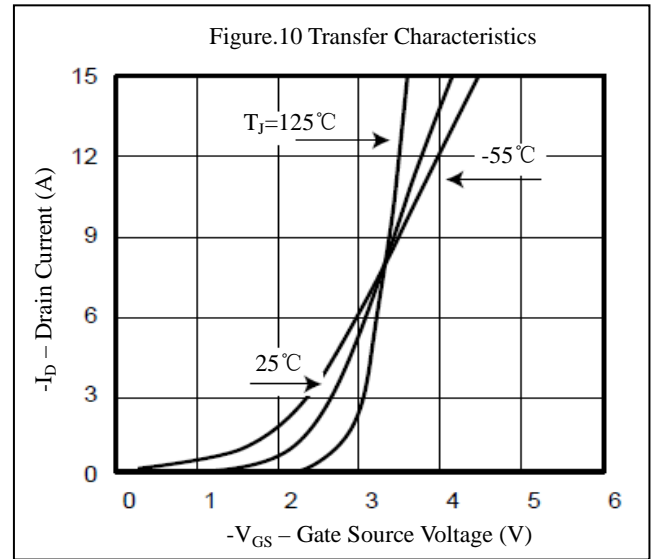
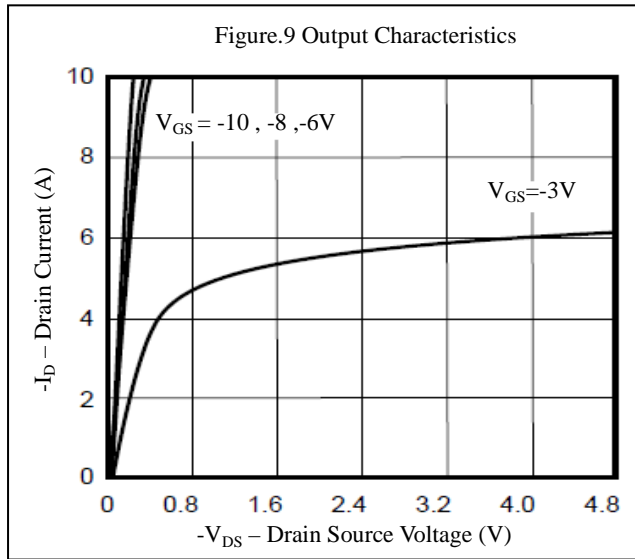
Characteristics Curve (N-Channel)



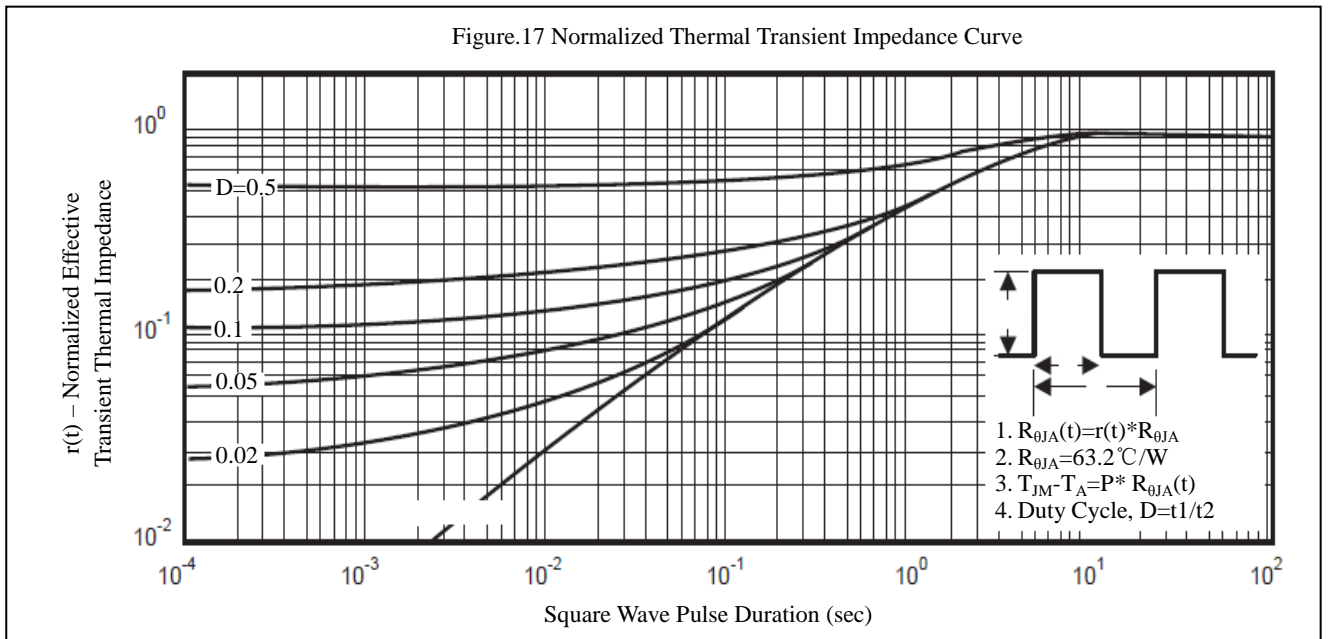
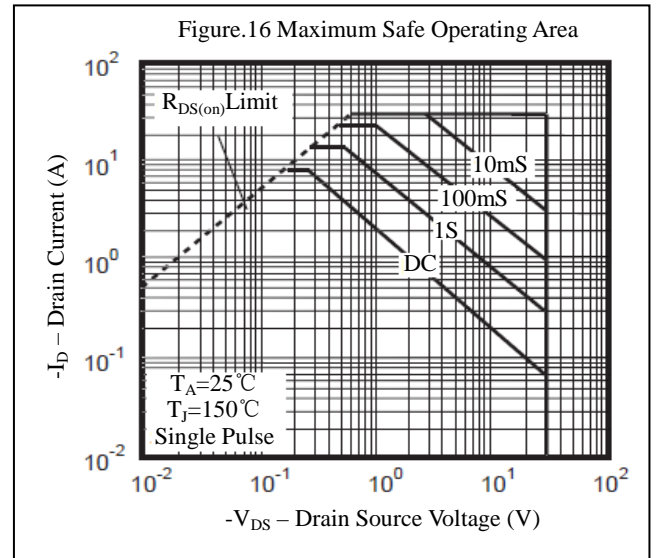
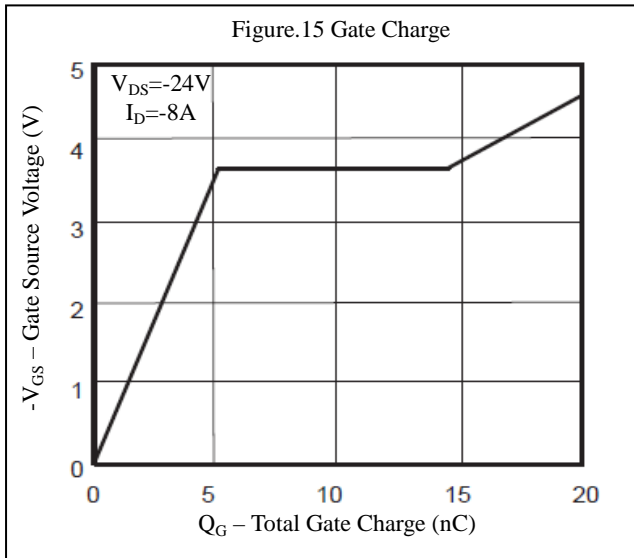
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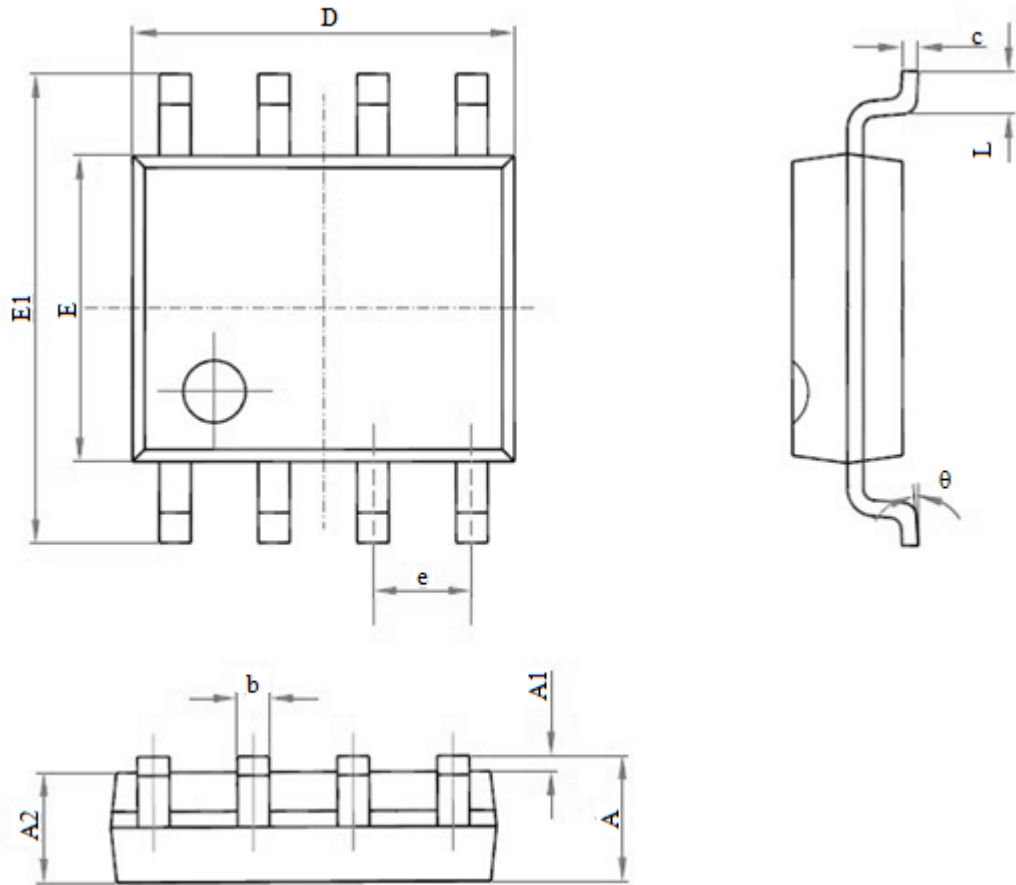
Characteristics Curve (P-Channel)



Characteristics Curve (P-Channel)



SOP-8 PACKAGE OUTLINE DIMENSIONS



Symbol	Dimensions In Millimeters (MM)		Dimensions In Inches (MIL)	
	Min	Max	Min	Max
A	1.350	1.750	0.053	0.069
A1	0.100	0.250	0.004	0.010
A2	1.350	1.550	0.053	0.061
b	0.310	0.510	0.013	0.020
c	0.170	0.250	0.006	0.010
D	4.700	5.100	0.185	0.200
E	3.800	4.000	0.150	0.157
E1	5.800	6.200	0.228	0.244
e	1.270(BSC)		0.050(BSC)	
L	0.400	1.270	0.016	0.050
θ	0°	8°	0°	8°



Notice

1. Specification of the products displayed herein is subject to change without notice. Continuous development may necessitate changes in technical data without notice. GEMMICRO or anyone on its behalf assumes no responsibility or liability for any errors or inaccuracies.

2. Stresses beyond those listed under “Absolute Maximum Ratings” may cause permanent damage to the device. These are stress ratings only and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications are not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.